

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gajadharsing et al.  
 App. No : 10/551,324  
 Filed : October 18, 2005  
 For : SEMICONDUCTOR TRANSISTOR  
 (DMOS) DEVICE FOR USE AS A  
 POWER AMPLIFIER  
 Examiner : Sarah Kate Salerno  
 Art Unit : 2814  
 Conf No. : 7460

CERTIFICATE OF EFS WEB  
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I hereby certify that this correspondence, and any other attachment noted on the automated Acknowledgement Receipt, is being transmitted from within the Pacific Time zone to the Commissioner for Patents via the EFS Web server on:

10/21/2008

(Date)



Christian A. Fox, Reg. No. 58,507

RESPONSE TO AUGUST 21, 2008 FINAL OFFICE ACTION

**Mail Stop AF**  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed August 21, 2008, Applicant respectfully submits the following:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 5 of this paper. A "Replacement Sheet" for each sheet of drawings being amended can be found enclosed herewith.

**Remarks** begin on page 6 of this paper.